Simulating Shuffle-Exchange Networks with P Systems

Rodica CETERCHI¹, Mario J. PÉREZ JIMÉNEZ²

¹Faculty of Mathematics and Computer Science, University of Bucharest Academiei 14, RO-010014, Bucharest, Romania

² Research Group on Natural Computing Department of Computer Science and Artificial Intelligence University of Sevilla

Avda. Reina Mercedes s/n, 41012 Sevilla, Spain E-mail: rc@funinf.cs.unibuc.ro, mario.perez@cs.us.es

Abstract. We present in this paper a simulation with P systems of the parallel architecture known as *shuffle-exchange network*. This will lead us to consider a new version of P systems with communication, for which the communication graphs are not fixed, but have a dynamic evolution, and for which different communication rules can be associated to different communication graphs. The simulation of the shuffle-exchange network provides a first, simple example, of such a system.

1 Introduction

Any computer, whether sequential or parallel, operates by executing instructions on data. An algorithm (*stream instructions*) tells the machine what to do at each moment, and the input of an algorithm (*stream data*) is affected by the instructions of the algorithm.

A parallel machine consists of a large number of processors (each one have an arithmetic logic unit with registers and a private memory) that be able to solve problems in a cooperative way; that is, that machine is capable of executing several instructions at the same time unit. Basically there are two different kinds of parallel machines according to the possibility to execute, simultaneously, either the same instruction of different data sets (SIMD) or different instructions on different data sets (MIMD).

In [1] deals with some sorting parallel algorithms, based on a comparison network model of computation, which incorporates features of parallelism, and a simulation of these algorithms through P systems with communication is presented.

In this paper we study the perfect shuffle **SIMD** computer where the processors interconection is based on the *shuffle* and *exchange* functions (that is, by a *shuffle–exchange* network), and we analyze the possibility to simulate this parallel architecture by a cellular computing with membranes.

The paper is organized as follows. In Section 2 we first give some preliminary notions on the perfect shuffle **SIMD** architecture, and some interesting properties of this parallel model are presented. In Section 3 we introduce a variant of P systems with communication modelling perfect shuffle machines. Section 4 and Section 5 illustrate the above simulation with parallel algorithms to sum integer numbers and to find the maximum of a finite

numbers of integer numbers. In the last section some conclusions and remarks of our study are presented.

References

- [1] Ceterchi, R.; Martín-Vide, C. P systems with Communication for Static Sorting. In M. Cavaliere, C. Martín-Vide and Gh. Păun (eds.), *Proceedings of the Brainstorming Week on Membrane Computing*, Report GRLMC 26/03, 2003, 101-117.
- [2] Păun, A., Păun, G.: The power of Communication: P Systems with Symport/Antiport. New Generation Computers, 20, 3(2002), 295–306.
- [3] Păun, G.: Computing with Membranes. Journal of Computer and System Sciences, 61, 1 (2000), 108–143, and Turku Center for CS-TUCS Report No. 208, 1998
- [4] Păun, G.: Membrane Computing. An Introduction, Springer-Verlag, Berlin, Heidelberg, 2002
- [5] Quinn, M.J. Parallel Computing. Theory and Practice, McGraw-Hill Series in Computer Science, 1994.